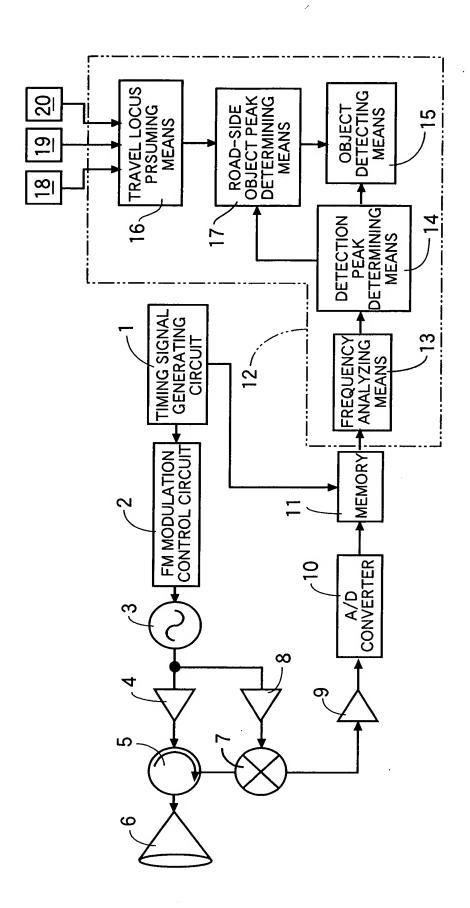
FOR VEHICLE

Inventor's Name: SAWAMOTO, et al.

Application No.: NEW Docket No.: 107348-00373



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FOR VEHICLE
Inventor's Name: SAWAMOTO, et al.
Application No.: NEW
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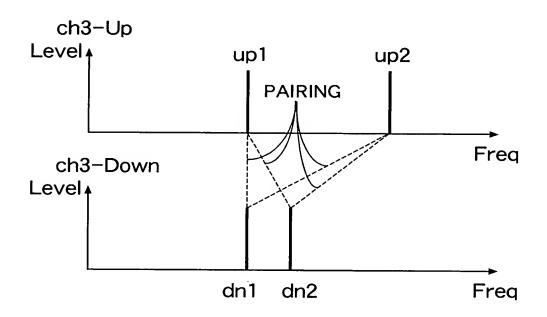
PRECEDING VEHICLE SUBJECT VEHICLE

Title: OBJECT-DETECTING SYSTEM

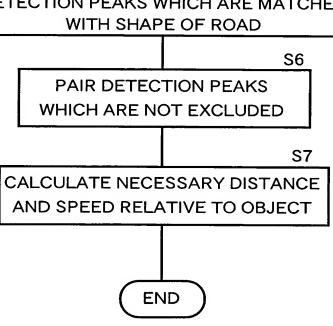
FOR VEHICLE

Inventor's Name: SAWAMOTO, et al.

FIG.3

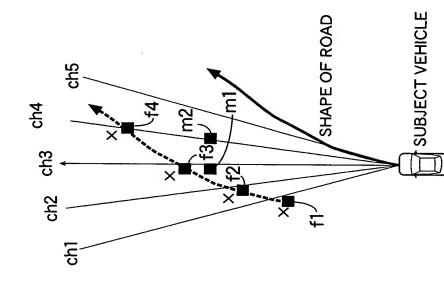


Title: UDJECT-DETECTING SYSTEM > FOR VEHICLE Inventor's Name: SAWAMOTO, et al. Application No.: NEW Docket No.: 107348-00373 FIG.4 **START S1** PRESUME SHAPE OF ROAD **S2** FFT PROCEDURE **S3** EXTRACT ASCENDING-SIDE PEAK (Fup) AND DESCENDING-SIDE PEAK (Fdn) FROM DETECTION SPECTRUM FOR EVERY ch **S4** PLOT ASCENDING-SIDE AND DESCENDING-SIDE PEAKS FOR EVERY ch IN TWO-DIMENSION S5 **EXCLUDE TWO-DIMENSIONALLY PLOTTED DETECTION PEAKS WHICH ARE MATCHED** WITH SHAPE OF ROAD **S6** PAIR DETECTION PEAKS

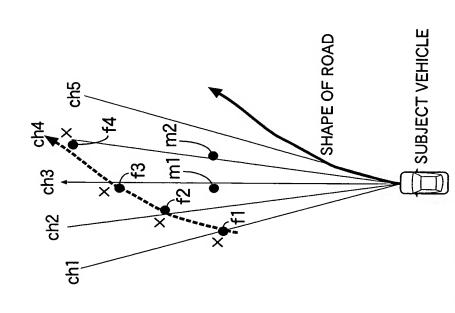


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FIG.5B



DESCENDING-SIDE DETECTION PEAK (INDICATED IN TWO-DIMENSION)



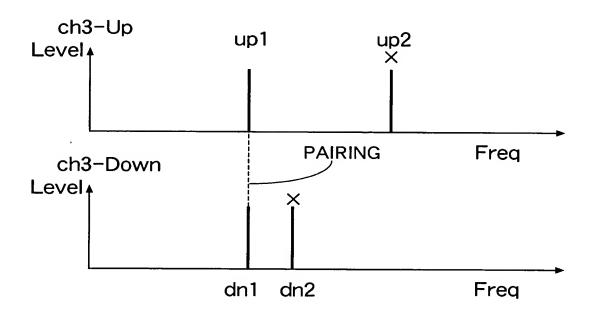
ASCENDING-SIDE DETECTION PEAK (INDICATED IN TWO-DIMENSION)

TITLE: UBJECT-DETECTING SYSTEM >

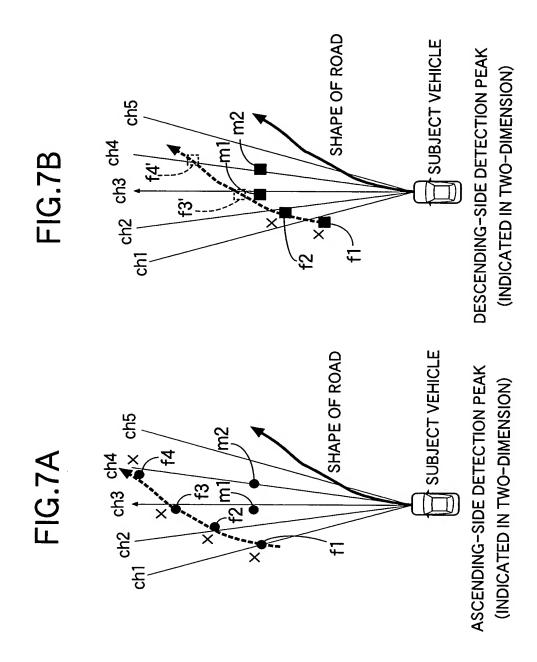
FOR VEHICLE

Inventor's Name: SAWAMOTO, et al.

FIG.6

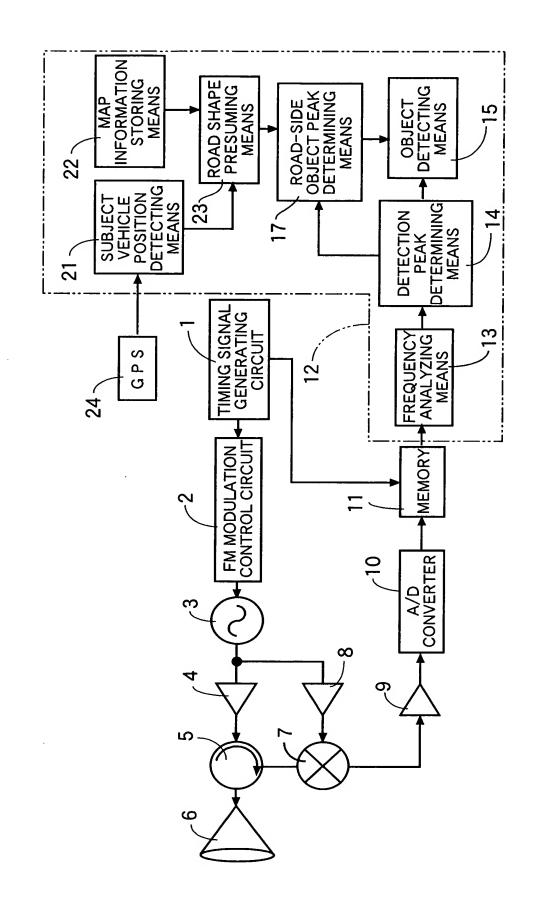


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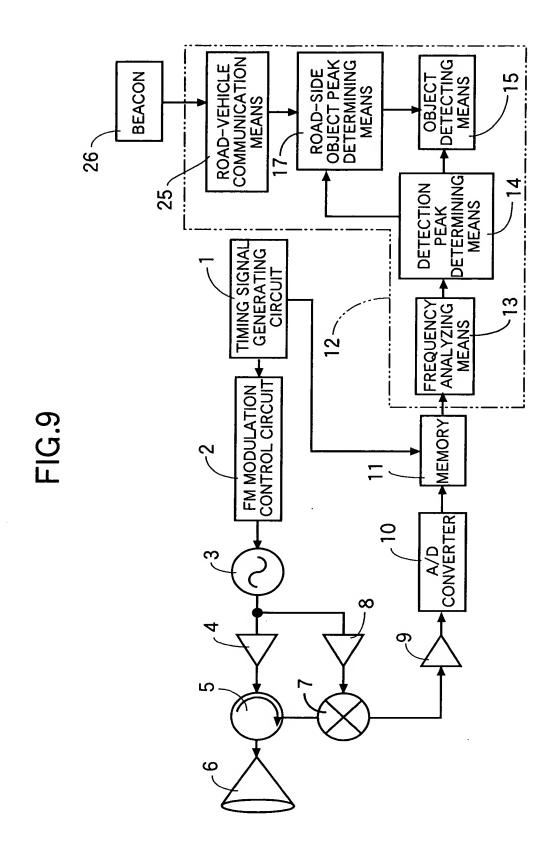


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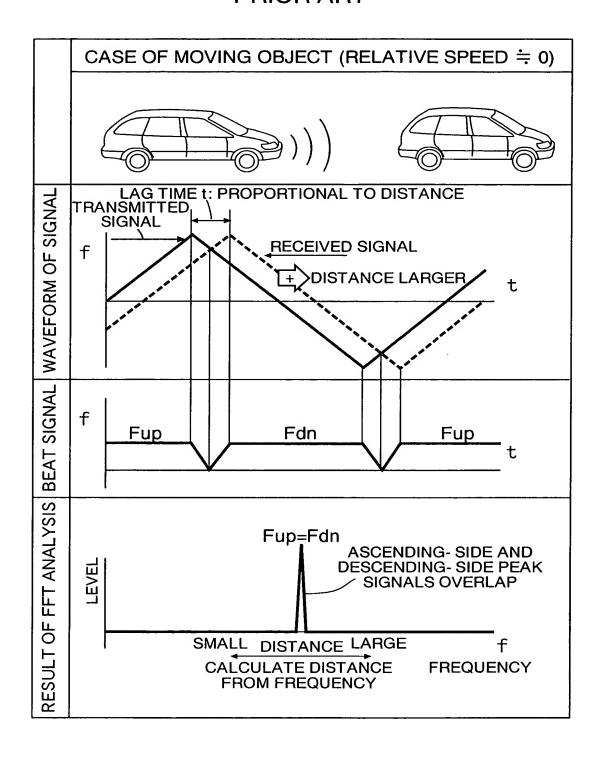
15 DETECTION PEAK DETERMINING MEANS TIMING SIGNAL GENERATING CIRCUIT ANALYZING MEANS FREQUENCY FM MODULATION CONTROL CIRCUIT 2 MEMORY 2 A/D CONVERTER ∞

FIG.10 PRIOR ART

FOR VEHICLE

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FIG.11 PRIOR ART

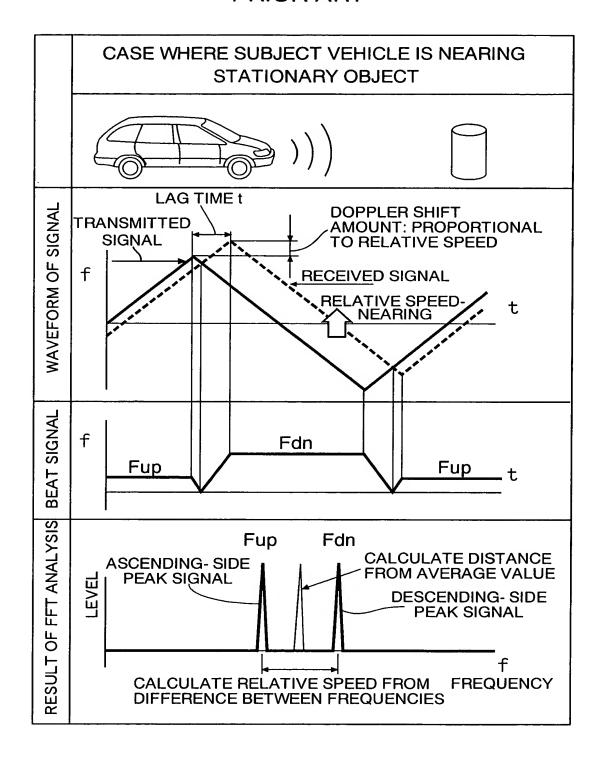


FOR VEHICLE

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FIG.12 PRIOR ART



FOR VEHICLE

Inventor's Name: SAWAMOTO, et al.

FIG.13 PRIOR ART

